

DOCUMENT-IDENTIFIER: US 5761669 A

TITLE: Controlling access to objects on multiple operating systems

CLPV:

a memory medium on which are stored machine instructions that are adapted to be executed on the computer network, for handling a request to change an access to an entity on said network by an account, where the entity may have existing inheritance attributes associated with it in connection with the account, the entity comprising one of a plurality of different types of entities that are controlled by at least one of a plurality of different operating systems on said network, said machine instructions, when executed on said network, implementing the following functions:

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw Desc	Image
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☒ 6. Document ID: US 5448746 A

L6: Entry 6 of 9

File: USPT

Sep 5, 1995

DOCUMENT-IDENTIFIER: US 5448746 A

TITLE: System for compounding instructions in a byte stream prior to fetching and identifying the instructions for execution

BSPR:

The inventions detailed herein teach in what manner there shall be accomplished the appending of control information to the instructions in the program to be executed where the base architecture has different attributes. It is to this object that the inventions which are here stated are addressed. We have provided a way for existing programs written in existing high level languages or existing assembly language program to be processed by software which can identify sequences of instructions which can be executed as a single compound instruction in a computer designed to execute compound instructions.

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw Desc	Image
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☐ 7. Document ID: US 5428786 A

L6: Entry 7 of 9

File: USPT

Jun 27, 1995

DOCUMENT-IDENTIFIER: US 5428786 A

TITLE: Branch resolution via backward symbolic execution

BSPR:

When backward symbolic execution for the computed destination address of a given execution transfer instruction reaches the entry point of a basic block, backward symbolic execution proceeds backward to each predecessor block that has not already been examined for the given execution transfer instruction. This last qualification avoids infinite loops in the backward symbolic execution. For this purpose, for example, an "epoch number" is incremented each time that backward symbolic execution is begun for a different given execution transfer instruction having a computed destination address, and that value of the epoch number is assigned to an epoch number attribute for each block when the backward symbolic execution is performed through each block.

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☐ 1. Document ID: US 6088437 A

L6: Entry 1 of 9

File: USPT

Jul 11, 2000

DOCUMENT-IDENTIFIER: US 6088437 A

TITLE: Call processing system, method and computer program product

CLPW:

a computer readable memory having computer readable instructions encoded therein that when executed by said data processor implement a central office switch characterization mechanism that characterizes signaling attributes of signals produced by one of a plurality of central office switches that service different geographical locations, said plurality of central office switches each having a different frequency and cadence signal event characteristic; and

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw Desc	Image
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☐ 2. Document ID: US 6083278 A

L6: Entry 2 of 9

File: USPT

Jul 4, 2000

DOCUMENT-IDENTIFIER: US 6083278 A

TITLE: System and method for displaying and editing assembly language source codes

DEPR:

The second block is one of the most important. It is the Resource Usage Block. It displays all of the data paths and functional units within the PP. Each of these resources is given a color to designate how it is being used in the current instruction. There are five colors or attributes: black signifies that one and only one of the parallel instructions requires the resource in order to execute, yellow means that the resource can be used in addition to those resources that are already used, green means that there is more than one way to encode the current instruction and that the resource is used in at least one, but not all, of the different encodings, gray means that the resource cannot be used unless at least one of the currently used resources is freed, and red means that the resource is trying to be used by more than one parallel instruction words, red means that more than one part of a parallel instruction is trying to use the resource.

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw Desc	Image
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☐ 3. Document ID: US 6014688 A

L6: Entry 3 of 9

File: USPT

Jan 11, 2000

DOCUMENT-IDENTIFIER: US 6014688 A

TITLE: E-mail program capable of transmitting, opening and presenting a container having digital content using embedded executable software

DEPR:

Creation executable software according to the present invention contains sequences of program instructions that allow for the creation of an enhanced E-mail message according to the present invention, as well as the transmission of the created message through the network to the recipient. Recipient executable software according to the present invention contains a different sequence of program instructions that allow for the receipt, and thus visual, audible and functional attributes to be obtained by the recipient.

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw Desc	Image
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☐ 4. Document ID: US 5918251 A

L6: Entry 4 of 9

File: USPT

Jun 29, 1999

DOCUMENT-IDENTIFIER: US 5918251 A

TITLE: Method and apparatus for preloading different default address translation attributes

DEPR:

As previously stated, alternative embodiments of the invention need not support different virtual regions (i.e., alternative embodiments of the invention do not use the region identifiers as address extensions). Also, while one embodiment is described in which a set of registers is used for storing the default translation attributes and in which the default translation attributes are selected from based on bits in the virtual address, alternative embodiments could store and/or select from the default translation attributes using any number of different techniques. For example, the default translation attributes may be: 1) stored by hardwiring the default translation attributes for certain or all of the regions; 2) stored by hardwiring the default translation attributes for certain or all virtual addresses (including a computer system that does not support virtual regions); 3) stored in a storage area outside of the processor and/or accessed by a software handler; 4) selected from based on the instruction currently associated with a virtual address (e.g., the execution of I/O instructions may result in the selection of one set of default translation attributes, while the execution of non-I/O instructions may result in the selection of a different set of default translation attributes); etc. The phrase "default translation attribute storage area" is used herein to refer to any mechanism for storing the default translation attributes. The phrase "default translation attribute selection criteria" is used herein to refer to any technique for selecting from the default translation attributes in the default translation attribute storage area.

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw Desc	Image
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☐ 5. Document ID: US 5761669 A

L6: Entry 5 of 9

File: USPT

Jun 2, 1998

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw Desc	Image
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☐ 8. Document ID: US 4731734 A

L6: Entry 8 of 9

File: USPT

Mar 15, 1988

DOCUMENT-IDENTIFIER: US 4731734 A

TITLE: Digital computer system incorporating object-based addressing and access control and tables defining derivation of addresses of data from operands in instructions

CLPW:

a return instruction specifying a return operation for terminating the execution of the procedure currently being executed, for locating the procedure whose execution was suspended, if said suspended procedure is in a different procedure object, for setting the representation of the current subject to represent the subject containing the domain of execution attribute for said different procedure object, and for resuming said suspended execution; and

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw Desc	Image
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☐ 9. Document ID: US 4675810 A

L6: Entry 9 of 9

File: USPT

Jun 23, 1987

DOCUMENT-IDENTIFIER: US 4675810 A

TITLE: Digital data processing system having a uniquely organized memory system using object-based addressing and in which operand data is identified by names accessed by name tables

CLPW:

a return instruction specifying a return operation for terminating the execution of the procedure currently being executed, locating the procedure whose execution was suspended to commence said current execution, if said located procedure is in a different said procedure object, setting the representation of said current subject to represent the subject containing the domain of execution attribute for said different procedure object, and resuming said suspended execution; and

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw Desc	Image
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